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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Attorney Docket: SLA0830

Inventor: Yoshi Ono, et al.

Confirmation No. 8642

Serial No.: 10/805,158

Group #: 2814

Filed: March 19, 2004

Examiner: Marcos D. Pizarro Crespo

Title: CHARGE TRAP NON-VOLATILE MEMORY STRUCTURE FOR
2 BITS PER TRANSISTOR

Commissioner for Patents
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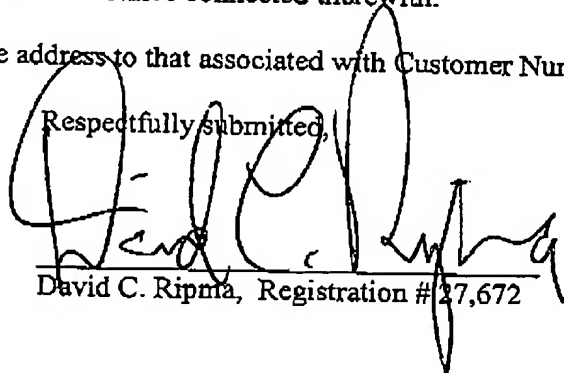
I hereby appoint Gerald Maliszewski, of P. O. Box 270829, San Diego, CA 92198-2829, U. S. Patent and Trademark Office Registration Number 38,054, as an associate attorney for the above-captioned matter, with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith.

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Date:

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Respectfully submitted,



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